

R E M A R K S

Careful review and examination of the subject application are noted and appreciated.

SUPPORT FOR CLAIM AMENDMENTS

Support for the amendments to the claims can be found in the drawings as originally filed, for example, in FIGS. 5-10 and 12, and in the specification as originally filed, for example, on page 17, line 15 through page 28, line 4, and on page 29, line 16 through page 30, line 3. As such, no new matter has been introduced.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 22 and 23 U.S.C. §102(e) being anticipated by Zhu et al. (U.S. Patent No. 6,948,127; hereinafter Zhu) has been obviated by amendment and should be withdrawn.

In contrast to the cited reference, the presently claimed invention (claim 1) provides:

a second circuit configured to generate one or more overlay images in response to the syntax elements . . . , where the one or more overlay images comprise at least one image selected from the group consisting of (i) an image displaying statistics about the decoded video signal including resolution, frame rate, bit rate, a bit rate graph, bitstream errors, peak signal to noise ratio (PSNR), average PSNR and a PSNR graph, where one or more scales of the bit rate graph and the PSNR graph are dynamically adjusted to display minimum and

maximum values, (ii) an image displaying a grid representing a number of macroblocks of the decoded video image, where for each macroblock a graphic symbol representing partitioning and encoding information for the respective macroblock is shown, (iii) an image displaying a grid representing a number of macroblocks of the decoded video image, where for each macroblock a graphic symbol representing a macroblock size parameter for the respective macroblock and a number representing a quantization parameter for the respective macroblock are shown, and (iv) an image displaying a grid representing a number of macroblocks of the decoded video image, where for each macroblock a graphic symbol representing a macroblock level adaptive frame/field (MBAFF) coding mode of the respective macroblock is shown.

Claims 22 and 23 include similar limitations. Zhu does not disclose or suggest the four images recited in claims 1, 22 and 23. Therefore, Zhu does not disclose or suggest each and every element of the presently claimed invention, arranged as in the claims, as required under MPEP §2131. As such, the presently claimed invention is fully patentable over the cited references and the rejection should be withdrawn.

CLAIM REJECTIONS UNDER 35 U.S.C. §103

The rejection of claims 1, 2, 8-18 and 20 under 35 U.S.C. §103(a) as being unpatentable over Zhu has been obviated by amendment and should be withdrawn.

The rejection of claims 3 and 4 under 35 U.S.C. §103(a) as being unpatentable over Zhu in view of Coleman et al. (U.S.

Patent No. 6,727,915; hereinafter Coleman) has been obviated by amendment and should be withdrawn.

The rejection of claims 5-7 under 35 U.S.C. §103(a) as being unpatentable over Zhu in view of Coleman in further view of Lau et al. (U.S. Patent No. 6,525,746; hereinafter Lau) has been obviated by amendment and should be withdrawn.

The rejection of claim 19 under 35 U.S.C. §103(a) as being unpatentable over Zhu in view of Everett et al. (U.S. Patent No. 6,532,024; hereinafter Everett) has been obviated by amendment and should be withdrawn.

The rejection of claim 21 under 35 U.S.C. §103(a) as being unpatentable over Zhu in view of Webb (U.S. Patent No. 7,167,522) has been obviated by amendment and should be withdrawn.

In contrast to the cited references, the presently claimed invention (claim 1) provides:

a second circuit configured to generate one or more overlay images in response to the syntax elements . . . , where the one or more overlay images comprise at least one image selected from the group consisting of (i) an image displaying statistics about the decoded video signal including resolution, frame rate, bit rate, a bit rate graph, bitstream errors, peak signal to noise ratio (PSNR), average PSNR and a PSNR graph, where one or more scales of the bit rate graph and the PSNR graph are dynamically adjusted to display minimum and maximum values, (ii) an image displaying a grid representing a number of macroblocks of the decoded video image, where for each macroblock a graphic symbol representing partitioning and encoding information for the respective macroblock is shown, (iii) an image

displaying a grid representing a number of macroblocks of the decoded video image, where for each macroblock a graphic symbol representing a macroblock size parameter for the respective macroblock and a number representing a quantization parameter for the respective macroblock are shown, and (iv) an image displaying a grid representing a number of macroblocks of the decoded video image, where for each macroblock a graphic symbol representing a macroblock level adaptive frame/field (MBAFF) coding mode of the respective macroblock is shown.

Claims 22 and 23 include similar limitations. The cited references do not appear to teach or suggest the group of four overlay images as presently claimed in claims 1, 22 and 23. Therefore, the cited references do not teach or suggest each and every element of the presently claimed invention as required under MPEP §2143. As such, the presently claimed invention is fully patentable over the cited references and the rejections should be withdrawn.

Claims 8, 10-15 and 18-21 depend, directly or indirectly, from claim 1 which is believed to be allowable. As such, the presently claimed invention is fully patentable over the cited references and the rejections should be withdrawn.

New claims 24-31 depend, directly or indirectly, from either claim 1 or claim 23 which are believed to be allowable. As such, the presently claimed invention is fully patentable over the cited references.

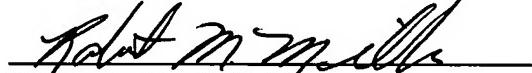
Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicants' representative between the hours of 9 a.m. and 5 p.m. ET at 586-498-0670 should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge Deposit Account No. 12-2252.

Respectfully submitted,

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